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# HP 64789C Emulator for the Intel 386EX Embedded Processor

## Product Overview

The HP 64789C emulator supports the Intel 80386EX processor with real-time, no wait state emulation to 25MHz in both real and protected modes. The emulator also fully supports the System Management Mode (SMM) operation. User selectable foreground and background monitors with dual port memory ensure maximum transparency in varied operating environments.

The HP 64789C active probe implementation operates between 3.3V and 5V and has long, 914 mm (36 inch), cables for easy target system access without sacrificing electrical transparency or clock speed. The probe contains an Intel 386EX microprocessor, emulation monitor, run control circuits and up to 8 Mbytes of emulation overlay memory. Extensive breakpoint capabilities provide considerable flexibility to start and stop program execution at specific locations within very complex code.

The emulation control and logic analyzer cards plug into the modular card cage common to all HP 64700 series emulators. Connection to the host computer occurs via LAN, RS-232 or RS-422. An easy to use real-time C debugger interface for the PC greatly simplifies the emulator command structure and allows even infrequent users to access the emulator's full potential.



### Features

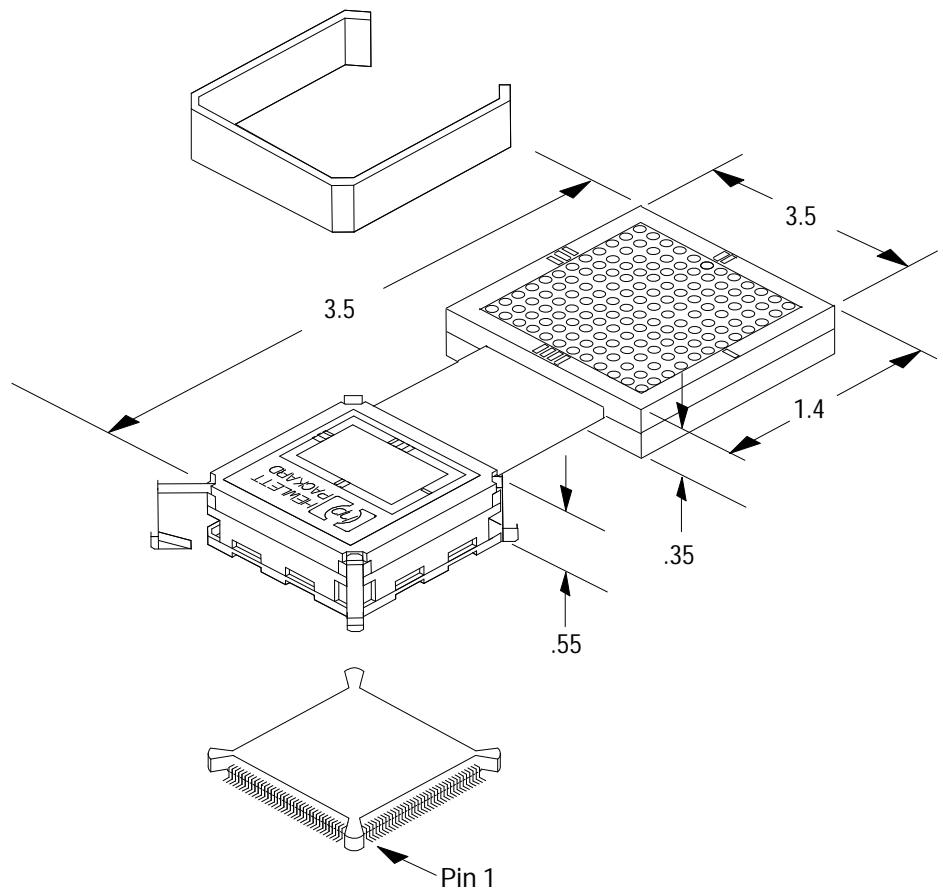
- Real-time zero wait state operation from emulation or target memory to 25MHz.
- Supports 3.3V to 5.5V operation.
- Active probe includes 8K of dual-port emulation memory and supports up to 8 Mbytes of additional emulation overlay memory.
- Demo board with both 132-pin PQFP and 144-pin TQFP versions of the Intel 386EX included for easy performance verification.
- Full symbolic debugging support for fast, easy development and debugging with HP user interface.
- Supports both real and protected modes to facilitate code reuse.
- Background and foreground monitors support all run control features and require minimal resources from the target system.
- Flash EPROMs for easy firmware updates.
- Unlimited software breakpoints.
- Four hardware execution or access breakpoints.
- 8K, 64K or 256K analyzer trace buffer supports real-time analysis of address, data and status/control information in conjunction with instruction dequeuing of the trace list.
- Symbols inserted in the trace list help you understand the code execution quickly.
- Display of current port and related timing information.
- Passive Mode Analysis operation allows emulator probe to simply replace the processor under test which helps isolate system problems from test equipment problems.

## Probe Adapter for HP 64789C

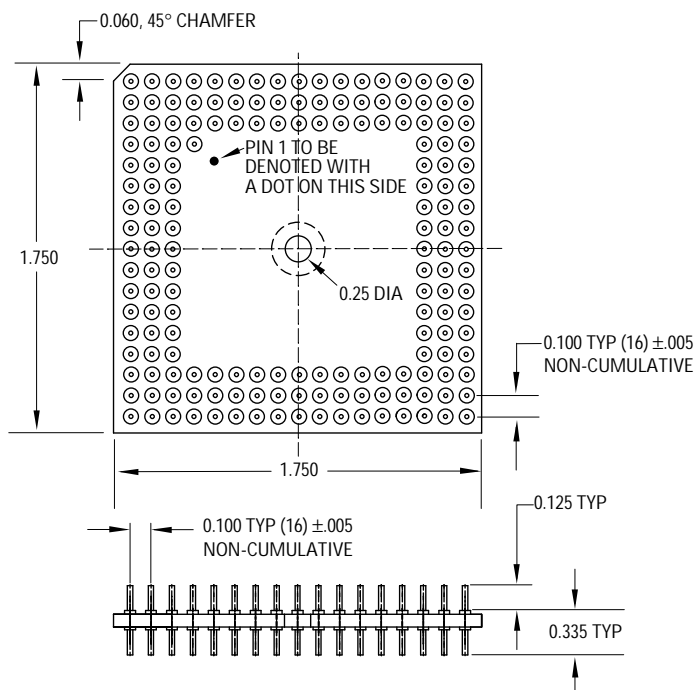
HP E3417A probe adapter for 132-pin PQFP:

The HP E3417A probe adapter connects to 132-pin PQFP versions of the Intel 386EX processor and converts the signals to the 13X13 PGA pattern required by the emulator probe. The probe adapter snaps onto the device under test (DUT). In addition, the adapter has four clips that slide under the DUT to secure the connection.

Connection between the female PGA pattern found on the adapter transition board and the female PGA interface on the emulator probe is accomplished with a male-to-male PGA header (HP Part Number 64774-87604) which is shipped with each emulator probe. For pin protection, one or more PGA sockets (HP part number 1200-1920) may be used.



**Figure 2.**  
HP E3417A Probe Adapter for 132-pin  
PQFP version of the Intel 386 EX



**Figure 1.**  
HP 64774-87604 PGA 13X13  
male-to-male header

## Specifications

<b>Processor Compatibility</b>	Intel 386EX in 132-pin PQFP or 144-pin TQFP configurations. Supports C Step.
<b>Electrical</b>	
<b>Maximum Clock Speed</b>	25MHz with no wait states required for emulation or target memory.
<b>Minimum ClockSpeed</b>	0Hz
<b>Power:</b>	500mA max from target system; all other power supplied by card cage.
<b>Vcc Range</b>	3.3V to 5.5V
<b>Environmental</b>	
<b>Temperature</b>	operating, 0.0 to +40.0 C (32.0 to +104.0 F); nonoperating, -40.0 to +70.0 C (-40.0 to +158.0 F).
<b>Altitude</b>	operating/nonoperating, 4600 m (15,000 ft.).
<b>Relative Humidity</b>	15% to 95%.

### Regulatory Compliance

(when installed in HP 64700 card cage)

<b>Electromagnetic Interference</b>	CISPR 11:1990/EN 55011 (1991): group 1 class A  IEC 801-2:1991/EN 50082-1 (1992): 4kV CD, 8kV AD  IEC 801-3:1984/EN 50082-1 (1992): 3V/m, 80% modulation, 26MHz - 1000MHz  IEC 801-4:1988/EN 50082-1 (1992): 0.5 kV signal lines, 1kV power lines.
<b>Safety</b>	self certified to UL 1244, IEC 348/HD 401 S1, CSA-C22.2 no.231 Series-M89

### Physical

<b>Cable Length</b>	probe to card cage approx. 914 mm
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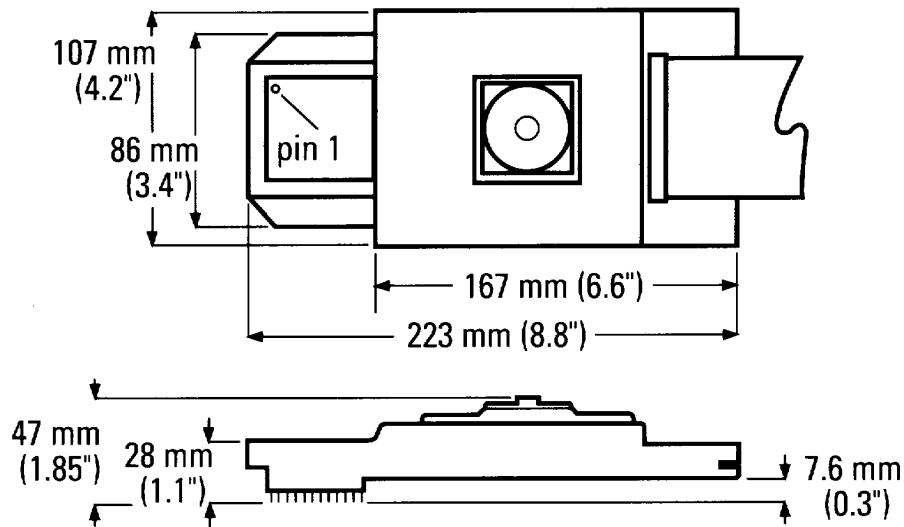


Figure 3. HP 64789C Emulator Probe Dimensions



## Development Tools for the Intel 386EX

Emulator	HP 64789C
Debugger/Emulator	HP B3637A Real Time C Debugger
Debugger Simulator	Green Hills Software Multi Environment
C Compiler	Green Hills Software Multi Environment Intel/MRI
Assembler	Intel/MRI
Linker Locator	Green Hills Software Multi Environment Intel/MRI

## Ordering Information

Terminal-based emulation system for Intel 386EX

Model	Description
HP 64789C	Active probe emulator with 8kbyte of dual-port emulation memory.
HP 64748C	Emulation control card
HP 64794A	8K-deep emulation bus analyzer, 80-channel
HP 64700B	Cardcage
HP B3637B	Real-time C Debugger
Emulation system options	
HP 64794C	64K-deep emulation bus analyzer, 80-channel
HP 64794D	256K-deep emulation bus analyzer
Probe Adapters	
HP E3417A	Connects to 132-pinPQFP
HP 64774-87604	PGA 13X13 male-to-male header
HP 1200-1920	PGA socket

Contact HP field engineer for information on 144-pin TQFP.

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<http://www.hp.com/go/tmdir>  
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